



The Cover

TOSHIBA REVIEW 1998 VOL.53 NO.11

Special Issues:

Arrival of the IP Era in LSIs

Special Reports

Arrival of the IP Era in LSIs

- Intellectual Property--A Key Concept in System LSIs
- **TREN** Technical Trends in IP Development
- Microprocessor Technologies in the System-on-Silicon Era
- Technologies for Embedded Memory Cores
- Development Technologies for Embedded System with Real-Time Operating System
- Middleware Technology
- Hardware/Software Codesign/Coverification Environment for Computer on Silicon
- TX System RISC Processor with Windows®CE
- MPEG2 System Decoder LSI for Set-Top Box
- Network LSIs
- 72 Mbit Rambus DRAM
- TPCS8004 2.5 V-Driven, n-Channel, Third-Generation Trench Gate MOSFET

Feature Articles

- JW 8020, JW-6020 Japanese Word Processors
- VOIXE BAR_{TM} DMR-120Z Digital Memory Recorder
- High-Throughput Inner lead bonder Equipment

Techno Notes

- Digital Broadcasting
-