# MICROWAVE POWER GaAs FET TIM7179-45SL

MICROWAVE SEMICONDUCTOR TECHNICAL DATA

#### **FEATURES**

- ·BROAD BAND INTERNALLY MATCHED FET ·HIGH POWER
- P1dB= 46.5dBm at 7.1GHz to 7.9GHz

#### ·HIGH GAIN

G1dB= 6.5dB at 7.1GHz to 7.9GHz

- •LOW INTERMODULATION DISTORTION IM3= -45dBc at Pout= 35.5dBm Single Carrier Level
- ·HERMETICALLY SEALED PACKAGE



#### RF PERFORMANCE SPECIFICATIONS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Output Power at 1dB Gain Compression Point	P1dB	VDS= 10V IDSset= 9.0A f = 7.1 to 7.9GHz	dBm	46.0	46.5	_
Power Gain at 1dB Gain Compression Point	G1dB		dB	5.5	6.5	
Drain Current	IDS1		А	_	9.6	10.8
Gain Flatness	ΔG		dB	_	_	±0.8
Power Added Efficiency	ηadd		%		36	
3rd Order Intermodulation Distortion	IM3	Two Tone Test Po= 35.5dBm, ∆f= 5MHz (Single Carrier Level)	dBc	-42	-45	
Drain Current	IDS2		А		9.6	10.8
Channel Temperature Rise	∆Tch	(VDS X IDS + Pin – P1dB) X Rth(c-c)	°C			100

Recommended Gate Resistance(Rg): 28  $\Omega$ 

## ELECTRICAL CHARACTERISTICS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Transconductance	gm	VDS= 3V IDS= 11.0A	S	_	8.0	_
Pinch-off Voltage	VGSoff	VDS= 3V IDS= 170mA	V	-1.0	-2.5	-4.0
Saturated Drain Current	IDSS	VDS= 3V VGS= 0V	А		24	
Gate-Source Breakdown Voltage	VGSO	IGS= -500μA	V	-5	_	
Thermal Resistance	Rth(c-c)	Channel to Case	°C/W		0.8	1.2

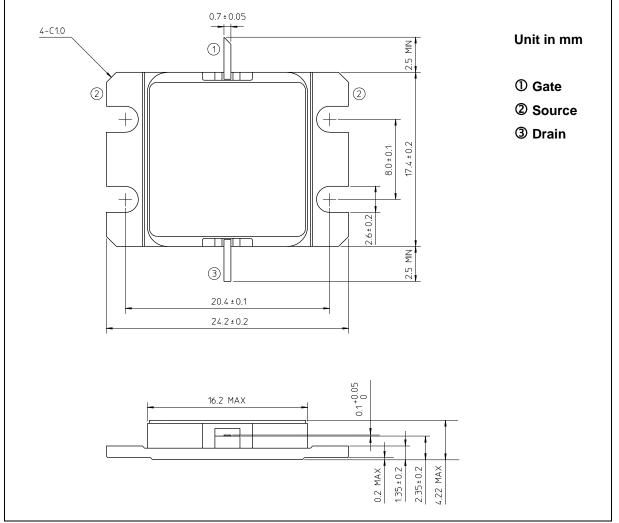
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#### ABSOLUTE MAXIMUM RATINGS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	UNIT	RATING
Drain-Source Voltage	VDS	V	15
Gate-Source Voltage	VGS	V	-5
Drain Current	IDS	А	20
Total Power Dissipation (Tc= 25°C)	PT	W	125
Channel Temperature	Tch	°C	175
Storage Temperature	Tstg	°C	-65 to +175

### PACKAGE OUTLINE (2-16G1B)

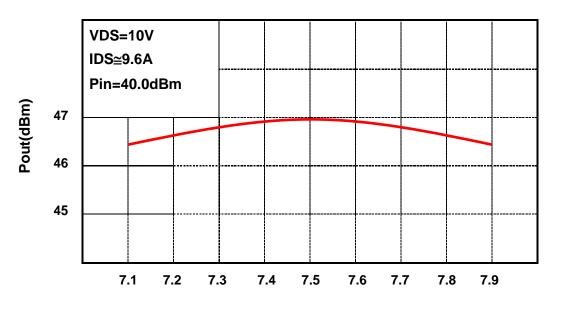


### HANDLING PRECAUTIONS FOR PACKAGE MODEL

Soldering iron should be grounded and the operating time should not exceed 10 seconds at 260°C or 3 seconds at 350°C.

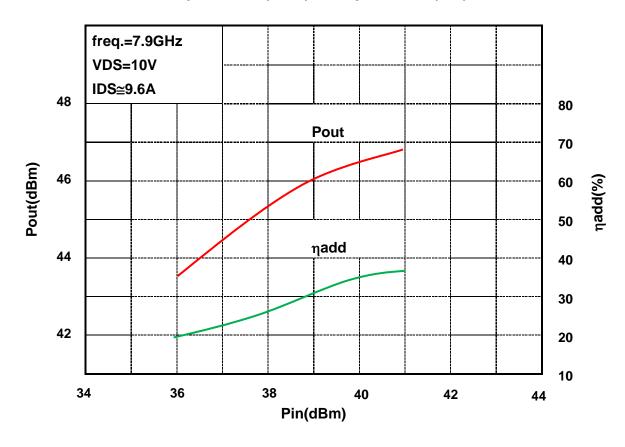
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#### **RF PERFORMANCE**



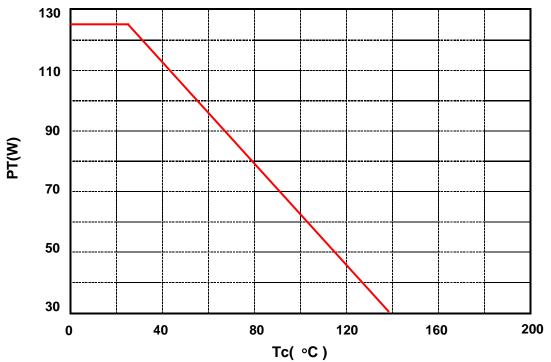
**Output Power (Pout) vs. Frequency** 

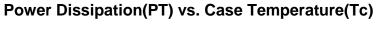
#### Frequency (GHz)



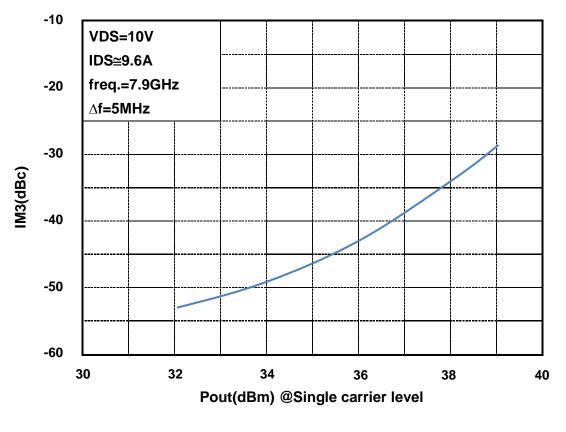
**Output Power(Pout) vs. Input Power(Pin)** 

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